


Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	352	soft adj programm\$	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L2	632	365/185.24.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L3	1372	365/185.29.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L4	349	365/185.3.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L5	1322	365/185.33.ccls.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L6	22	1 and 2	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L7	62	1 and 3	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L8	33	1 and 4	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L9	59	1 and 5	US-PGPUB; USPAT	OR	ON	2006/01/20 15:56
L10	0	malfa-antonino.in.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:57
L11	6	la-malfa-antonino.in.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:57
L12	6	messina-marco.in.	US-PGPUB; USPAT	OR	ON	2006/01/20 15:57

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Date: 1/20/2006


**PALM INTRANET**

Time: 15:58:59

## Inventor Name Search Result

Your Search was:

Last Name = LA MALFA

First Name = ANTONINO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">10623474</a>	Not Issued	41	07/18/2003	Automatic decoding method for mapping and selecting a non-volatile memory device having a LPC serial communication interface in the available addressing area on motherboards	LA MALFA, ANTONINO
<a href="#">10686552</a>	<a href="#">6922362</a>	150	10/15/2003	STRUCTURE FOR UPDATING A BLOCK OF MEMORY CELLS IN A FLASH MEMORY DEVICE WITH ERASE AND PROGRAM OPERATION REDUCTION	LA MALFA, ANTONINO
<a href="#">10748447</a>	<a href="#">6885584</a>	150	12/30/2003	FAST PAGE PROGRAMMING ARCHITECTURE AND METHOD IN A NON-VOLATILE MEMORY DEVICE WITH AN SPI INTERFACE	LA MALFA, ANTONINO
<a href="#">10779856</a>	Not Issued	71	02/17/2004	Method for soft-programming an electrically erasable nonvolatile memory device, and an electrically erasable nonvolatile memory device implementing the soft-programming method	LA MALFA, ANTONINO

Inventor Search Completed: No Records to Display.

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# **PALM INTRANET**

Time: 15:59:19

## Inventor Name Search Result

Your Search was:

Last Name = MESSINA

First Name = MARCO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">09196913</a>	<a href="#">6054932</a>	150	11/20/1998	LED TRAFFIC LIGHT AND METHOD OF MANUFACTURE AND USE THEREOF	MESSINA, MARCO
<a href="#">10199965</a>	Not Issued	41	07/18/2002	Hybrid architecture for realizing a random numbers generator	MESSINA, MARCO
<a href="#">10270020</a>	Not Issued	41	10/11/2002	Random bit sequence generator	MESSINA, MARCO
<a href="#">10447293</a>	<a href="#">6785174</a>	150	05/28/2003	TESTING METHOD AND DEVICE FOR NON-VOLATILE MEMORIES HAVING A LPC (LOW PIN COUNT) COMMUNICATION SERIAL INTERFACE	MESSINA, MARCO
<a href="#">10779856</a>	Not Issued	71	02/17/2004	Method for soft-programming an electrically erasable nonvolatile memory device, and an electrically erasable nonvolatile memory device implementing the soft-programming method	MESSINA, MARCO
<a href="#">60307002</a>	Not Issued	159	07/20/2001	Hybrid architecture for realizing a random numbers generator	MESSINA, MARCO

Inventor Search Completed: No Records to Display.

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